Ragkor

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Education

Bachelors of Electrical Engineering (CO-OP) ,Expected 2017
SKY HIGH
Dean's Honors List
GPA: 3.70

Skills

Software

- C/C++, Python
- HTML, JQuery
- System Verilog ,VHDL
- Orcad PSpice , Eagle CAD
- Altium Designer

Hardware

- Lab equipment: Scopes, Multimeters
- PCB Design and Testing
- Altera Cyclone 2 FPGA , ATMEL 128 and Freescale HCS012
- RTL Design and verification.

Relevant Experience

Course Project: Hardware Based Image Decompressor
Sky high , electronics for dummmies *Sep 2015 to Present*

- Designed a FSM using a Cyclone 2 based FPGA.
- Learned to analyze timing issues with low level hardware and software interfacing.
- Gained experience with software verification for hardware designs.

Undergraduate Research Assistant
SKY HIGH , ECE Department *May 2015 to Sep 2015*

- Coded a Python script to automatically collect target information from more than 8000 webpages pages.
- Developed a file generator to aggregate individual datasets into a single searchable file.
- Developed a quiz style tool to optimize GUI design using HTML and JQuery

Summer Intern
Telephones . *May 2014 to Aug 2014*

- Tested and verified PCB's using standard lab equipment such as oscilloscopes and multimeters.
- Worked with senior engineers designing test benches for power supplies.

Projects and Activities

First Engineering Competition, Software Challenge

- Won 1st place in a university wide software competition.
- Built a cabin pressure simulator, in less than 24 hours, with a test bed and fully functional user interface.

Second Engineering Competition – Software Challenge

- Represented the university at the provincial level

Motor Controller PCB

- Designed custom PCB to isolate high current sensors and motors from microcontroller