SARANSH PARASHAR

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CAREER OBJECTIVE

Seeking a **challenging work environment** that encourages **continuous learning** and provides insights for new ideas, thereby providing me an opportunity for where I can utilize my skills acknowledge efficiently for **organizational growth.**

ACADEMIC PROFILE

Degree	Discipline	school/college	Board/ University	Percentage	Year of Passing
B. E.	Electronics And Communication Engineering	CMR Institute of technology	Visvesvaraya Technological University	67.02	2014
12th	PCM	Saraswati Vidya Mandir,Munger	CBSE	83.4	2009
10th	CBSE	ShyamLal DAV Public school,Khagaria	CBSE	90.2	2007

Projects and Experience

Work Experience -1.2 year in **Accenture**

Training- I am trained in accenture on ASP.NET web technologies and did a mini project in the same technology which included implementation of sql , Stored Procedures ,ADO.NET, ASP.NET.

Within Accenture I am working on Mobile Application development using .Net with Xamarin Cross Development framework.I have worked on two projects.

1.Android Project

Project Name- Showcase for Sensors

Technology-Xamarin Android

Project Duration-6 months

Roles and Responsibilities-I worked on android platform for creation of App to connect with sensors which included receiving data from beacons to the app,material design, connection with the server using Volley.

2.Pearson Project

Project Name- Pearson App for kindergarten

Technology- Xamarin Ios, MvvmCross

Project Duration -4 months(and counting)

Roles and Responsibilities-I am currently working on the project of Pearson Education on the **MvvmCross Platform**.The architecture used is **MVVM**.The app is built using Xamarin Ios .The app is based on MvvmCross platform. My responsibilities include mainly fixing the bugs.

ACADEMIC PROJECTS

1. Low power implementation of electrocardiogram (ECG) QRS detector for body sensor networks:

Baseline wandering and background noise are removed from original ECG signal by a mathematical morphological method. Then the multi pixel modulus accumulation is employed to act as a low-pass filter to enhance the QRS complex and improve the signal-to-noise ratio. Corresponding power and area efficient VLSI architecture is designed and implemented on a commercial nano-FPGA

TECHNICAL SKILLS

- C#
- Xamarin.Android,Xamarin.Ios
- Android
- Java

AREAS OF INTEREST

- Software Programming

- Networking, Analog Electronics

ACHIEVEMENTS AND EXTRACURRICULAR ACTIVITIES

- Member of Organizing team for college Cultural fest [cultra-2012].
- Organiser of Paper Presentation event in College Technical festival (2012-2013).
- Runner up of Table Tennis championship in College Sports Meet.
- Second rank in Secondary Board Examination in district.
- Represented School in various district level Cricket tournaments.

INTERESTS AND HOBBIES

- Playing Table Tennis, Cricket.
- Travelling and writing Blogs.
- Reading novels, Newspaper, Reading articles on Politics and social-welfare.

PERSONAL INFORMATION

Father's Name : Mr. Vinay kumar pandey
Mother's Name : Mrs. Shila pandey
Date of Birth : 20 SEP 1992
Gender : Male
Nationality : Indian
Languages known : English, Hindi
Address **:** 316,vansi richfields apartment
marathalli bridge
Bangalore